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# Lessons Learned using the JNTF Verification & Validation Process

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# Overview

- ♦ The National Missile Defense (NMD) High Fidelity System Simulation (HFSS)
- ♦ JNTF V&V Methodology Overview
- ♦ Tailoring
- ♦ Lessons Learned
- ♦ Summary

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# HFSS Overview

- ♦ HFSS is an end-to-end NMD simulation (Threat Launch to Negation or Impact)
- ♦ HFSS intended use
  - Systems engineering studies
  - System performance verification
- ♦ Developed by the NMD Systems Engineering Contractor
- ♦ Verification and Validation performed by the JNTF and the Naval Surface Warfare Center

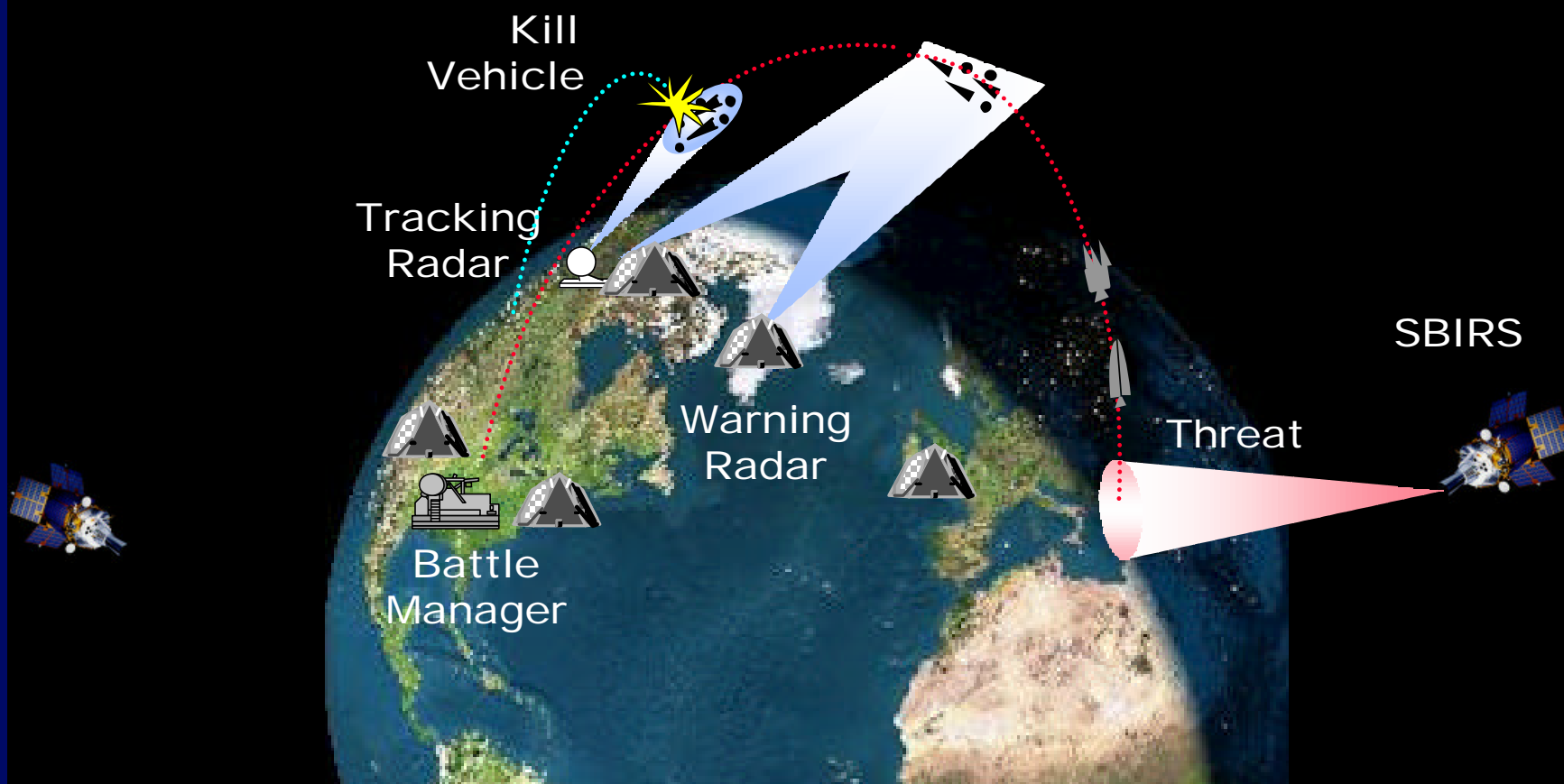
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# Overview

## National Missile Defense



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# V&V Constraints

Limited  
Documentation

Distributed  
Team

Preliminary  
Release

Evolving  
System

Early  
Assessment  
Required

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# JNTF V&V Methodology

## 39 Procedures Available

Conceptual Validation	Software Verification	Operational Validation	Data Validation
<ul style="list-style-type: none"><li>• Development history analysis</li><li>• Model derivative analysis</li><li>• Previous model use analysis</li><li>• Requirements criticality analysis</li><li>• System analysis</li><li>• Modeling concepts analysis</li><li>• Input/output fidelity analysis</li><li>• Algorithm analysis</li><li>• Logic trace analysis</li></ul>	<ul style="list-style-type: none"><li>• CASE and design methodology adherence analysis</li><li>• Screening procedure</li><li>• Process metrics analysis</li><li>• Products metric analysis</li><li>• Internal software testing analysis</li><li>• Code analysis</li><li>• Security code analysis</li><li>• QA review</li><li>• Documentation review</li><li>• User support review</li><li>• Model flexibility assessment</li><li>• CM Review</li><li>• IV&amp;V Review</li><li>• Graphical display evaluation</li></ul>	<ul style="list-style-type: none"><li>• Animation test</li><li>• Fixed value test</li><li>• Simplified assumption testing</li><li>• Extreme condition testing</li><li>• Predictive validation testing</li><li>• Output validation analysis</li><li>• Comparison to test data</li><li>• Sensitivity analysis</li><li>• Feedback loop analysis</li><li>• Event sequencing testing</li><li>• Head to head comparison</li><li>• Input/output relationship analysis</li></ul>	<ul style="list-style-type: none"><li>• Input data analysis</li><li>• Data consistency analysis</li><li>• Portrayal of constants analysis</li><li>• Distribution form analysis</li></ul>

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# JNTF V&V Methodology

## 4 Procedures Selected

Conceptual Validation	Software Verification	Operational Validation	Data Validation
<ul style="list-style-type: none"><li>Development history analysis</li><li>Model derivative analysis</li><li>Previous model use analysis</li><li>Requirements criticality analysis</li></ul> <p><b>Modeling Concept Analysis</b></p> <ul style="list-style-type: none"><li>Input/output analysis</li><li>Algorithm analysis</li><li>Logic trace analysis</li></ul>	<ul style="list-style-type: none"><li>CASE and design methodology adherence analysis</li><li>Screening procedure</li><li>Process metrics analysis</li><li>Products metric analysis</li><li>Internal software testing analysis</li><li>Code analysis</li><li>Security code analysis</li><li>QA review</li><li>Documentation review</li><li>User support review</li><li>Model flexibility assessment</li><li>CM Review</li><li>IV&amp;V Review</li><li>Graphical display evaluation</li></ul>	<ul style="list-style-type: none"><li>Animation test</li><li>Fixed value test</li><li>Simplified assumption testing</li><li><b>Extreme Condition Test</b></li><li>Predictive validation testing</li><li>Output validation analysis</li><li>Comparison data</li><li><b>Sensitivity Analysis</b></li><li>Feedback loop analysis</li><li>Event sequencing testing</li><li>Head to head comparison</li><li>Input/output relationship analysis</li></ul>	<p><b>Input Data Analysis</b></p> <ul style="list-style-type: none"><li>Database analysis</li><li>Portrayal of constants analysis</li><li>Distribution form analysis</li></ul>

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# JNTF V&V Procedures

## Specific Procedures:

Model Concepts Analysis	Input Data Analysis
Threat SBIRS Interceptor UEWR XBR BMC3 IFICS Environment	Threat Scenario 1
Extreme Condition	Sensitivity Analysis
Ideal Performance  Degraded Performance	Interceptor SBIRS  Battle Manager Radar

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# Lessons Learned



Communications. VTCs and Telecons. E-Mail Relay. Regular Visits and team coordination meetings. On-site presence at the development site

Computer Resources. Start early on acquisition and configuration of H/W and S/W (especially COTS).



V&V Readiness Review. Pause to assess what you have accomplished, and review what lies ahead.

Pre-defined Methodology lets you focus on the engineering problems.



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# Lessons Learned

Document Production. With a team, you have different format and submittal requirements. Give organizational credit.



Final Assessment and Interpretation of results. Half empty or half full ? The Intended Use is critical.

Do not apply standards for Operational Software to a Computer Simulation



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# The Art of V&V

## The Art of War: Its Application to V&V of Models and Simulations

- ♦ **OBJECTIVE:** You and the customer must agree on the intended use
- ♦ **PLANNING:** Prepare clear, uncomplicated plans and procedures
- ♦ **OFFENSIVE:** Demand early, unofficial deliveries
- ♦ **MASS:** People and computers must be in place and ready
- ♦ **ECONOMY OF FORCE:** Do not get distracted by problems or issues unrelated to the Intended Use
- ♦ **MANEUVER:** Be flexible and adaptable to the technical and programmatic problems encountered late in the process

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# SUMMARY

- ◆ Procedures can be selected based on customer constraints and needs
- ◆ A pre-defined methodology means resources used for engineering instead of process design
- ◆ The JNTF methodology results in a high quality product even in a resource constrained environment

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